

03500.018041.



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
: Monica D. Harrison  
MAKOTO KUBOTA, et al. )  
: Group Art Unit: 2813  
Application No.: 10/541,495 )  
:  
371(c) Date: July 7, 2005 )  
:  
For: ORGANIC SEMICONDUCTOR )  
DEVICE : July 13, 2007

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Attention Docket Clerk: Code REAS**

COMMENTS ON STATEMENT  
OF REASONS FOR ALLOWANCE

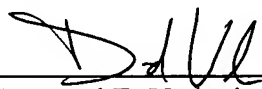
Sir:

Applicants are in receipt of a Notice of Allowance dated June 25, 2007 in the above-referenced application. The issue fee is due on September 25, 2007 and is being paid concurrently herewith.

Applicants respectfully traverse the Reasons for Allowance set forth in the Notice of Allowability on the grounds that independent Claims 1 and 13 do not recite the language "a gate insulating layer on a resin substrate that operates at a low voltage without causing a shortage of electric charges at the interface between the gate insulating layer and the organic semiconductor due to printing or spin coating and through low temperature heat treatment."

Applicants' undersigned attorney may be reached in our Costa Mesa, California, office at (714) 540-8700. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,

  
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Damond E. Vadnais  
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